

What is claimed is:

1. An active matrix display driving circuit is disclosed and a driving circuit of each pixel on a display panel includes:

5 a first scan transistor and a second scan transistor, the gates (G) of the first scan transistor and second scan transistor connected to a scan line and sources (S) connected to a data line;

 a driving transistor, the source (S) of the driving
10 transistor connected to a voltage supply;

 a connect transistor, the source (S) of the connect transistor connected to drains (D) of driving transistor and second scan transistor and gate (G) connected to an emission line;

15 a first switch transistor, the source (S) of the first switch transistor connected to a first voltage supply and gate (G) connected to a scan line;

 a second switch transistor, the source (S) of the second switch transistor connected to a second voltage
20 supply and gate (G) connected to the emission line;

 a storage capacitor, one end of the storage capacitor connected to drains (D) of the first switch transistor and second switch transistor and the other end connected to drain (D) of the first scan transistor and gate (G) of
25 the driving transistor;

- a luminescence device, the anode of the luminescence device connected to drain (D) of the connect transistor and the cathode connected to the ground.
2. The active matrix display driving circuit according to
5 claim 1, wherein the first scan transistor, second scan transistor, driving transistor, connect transistor, first switch transistor and second switch transistor are PMOS transistors.
3. The active matrix display driving circuit according to
10 claim 1, wherein the luminescence device is an electro-luminescence device (EL device).
4. The active matrix display driving circuit according to claim 1, wherein the voltage of second voltage supply is greater than that of first voltage supply.
- 15 5. The active matrix display driving circuit according to claim 1, wherein the first voltage supply can be connected to the emission line and the second voltage supply can be connected to the scan line.
6. The active matrix display driving circuit according to
20 claim 1, wherein the first voltage supply can be connected to voltage supply.
7. The active matrix display driving circuit according to claim 1, wherein the first voltage supply can be connected to voltage supply and the second voltage
25 supply can be connected to the scan line.

8. An active matrix display driving circuit is disclosed and a driving circuit of each pixel on a display panel includes one scan line and one data line as follows:

5 a first scan transistor and a second scan transistor, the gates (G) of the first scan transistor and second scan transistor connected to a scan line and sources (S) connected to a data line;

a driving transistor, the source (S) of the driving transistor grounded;

10 a connect transistor, the source (S) of the connect transistor connected to drains (D) of driving transistor and first scan transistor and gate (G) connected to an emission line;

15 a first switch transistor, the source (S) of the first switch transistor connected to a first voltage supply and gate (G) connected to a scan line;

a second switch transistor, the source (S) of the second switch transistor connected to a second voltage supply and gate (G) connected to the emission line;

20 a storage capacitor, one end of the storage capacitor connected to drains (D) of the first switch transistor and second switch transistor and the other end connected to drain (D) of the second scan transistor and gate (G) of the driving transistor;

25 a luminescence device, the anode of the luminescence

device connected to voltage supply and cathode connected to drain (D) of the connect transistor.

9. The active matrix display driving circuit according to claim 8, wherein the first scan transistor, second scan
5 transistor, driving transistor, connect transistor, first switch transistor and second switch transistor are NMOS transistors.

10. The active matrix display driving circuit according to claim 8, wherein the luminescence device is an
10 electro-luminescence device (EL device).

11. The active matrix display driving circuit according to claim 8, wherein the voltage of second voltage supply is less than that of first voltage supply.

12. The active matrix display driving circuit according to
15 claim 8, wherein the first voltage supply can be connected to the emission line and the second voltage supply can be connected to the scan line.

13. The active matrix display driving circuit according to
20 claim 8, wherein the first voltage supply can be grounded.

14. The active matrix display driving circuit according to claim 8, wherein the first voltage supply can be grounded and second voltage supply can be connected to the scan line.

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